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6, NL-5656 AA Eindhoven (NL). **DE HAAS, Clemens, G., J.** [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). **BOLLEN, Gerrit, J.** [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). **VISSEUR, Ruurd, A.** [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

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(74) Agents: **ELEVLD, Koop, J.** et al.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

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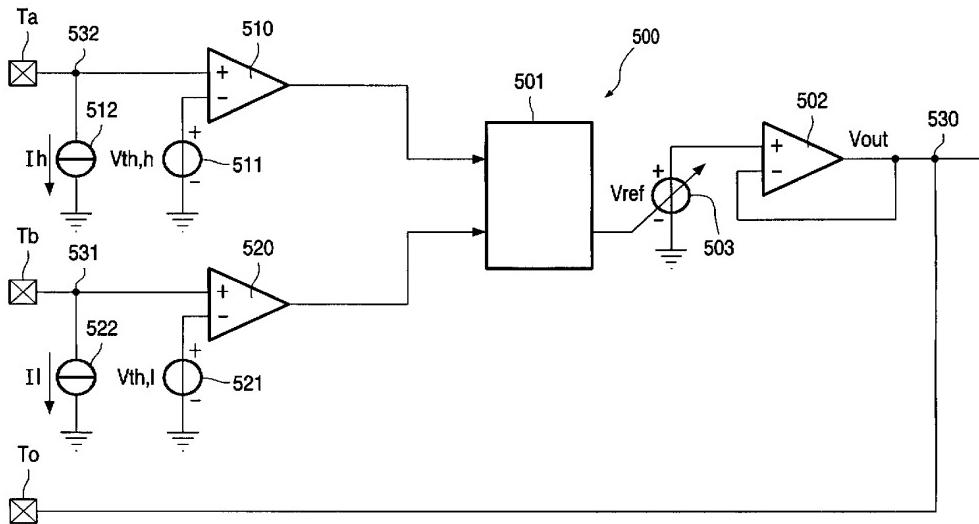
(71) Applicant (for all designated States except US): **KONINKLIJKE PHILIPS ELECTRONICS N.V.** [NL/NL];
Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **BOOMKAMP, Aloysius, J., M.** [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). **VOSS, Harm, G., J.** [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). **BUTSELAAAR, Stefan, G., E.** [NL/NL]; c/o Prof. Holstlaan

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(54) Title: VOLTAGE REGULATOR CIRCUIT ARRANGEMENT



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(57) Abstract: The invention relates to a voltage regulator circuit arrangement comprising a voltage regulator for generating an output voltage in dependence of a reference signal, characterized in that a reference signal generation circuit is provided for generating said reference signal comprising a plurality of inputs connected to internal terminals, whereby a sub-set of said plurality internal terminals is connected to an external terminal.



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